

Geoffrey Hale

Engineering Manager

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Skills

Project Management

- Certified in Earned Value Management 05/2011
- Developed IMS for EMS Enterprise Campaign
- Led team of 10 engineers, all members received Team Achievement Awards 11/2010

Engineering

- System, hardware and software test and troubleshooting
- PCB design and verification
- C++, VBA, MATLAB, HTML, CSS
- Microsoft Project, Excel, Access with VBA, Visio, Power Point, Word

Points of Distinction

- Member of SAS Engineering Rotation Program (ERP) Class of 2012
- Presented at Raytheon ISaC Symposium 2011: "SharePoint 2010 No Code Solutions" 06/2011
- Raytheon Innovation Challenge 2011: Represented VIPER in Orlando, FL 06/2011
- Shining Stars: Performance Excellence, Teamwork and Collaboration

ATEP

- Introduction to RF and Microwaves 06/2011
- Airborne Radar Introduction 07/2011

Experience

Raytheon SAS: ERP: SVC JSF LRIP

Plano, TX

CAM/IPTL, SVC

01/2012 – present

- Managing cost accounts and integrated master schedules.
- Leading cross-discipline engineering product teams.

Raytheon SAS: ERP: IT Solutions Architecture

El Segundo, CA

Engineering Rotator, IT

04/2011 – 01/2012

- Developed IMS for EMS Enterprise Campaign.
- Maintained EV-related data for Cyber EC, RFSD EC, and EMS ET.

Raytheon SAS: ERP: One IDE SharePoint Team

Goleta and El Segundo, CA

Engineering Rotator, Engineering

09/2010 – 03/2011

- Led team of 10 engineers to design SharePoint solutions for Cost Modeling and Earned Value.

Raytheon SAS: EW: Power Solutions Design

Goleta, CA

Electrical Engineer

01/2010 – 02/2011

- Led Project to track department productivity using MS Access database and VBA.
- Tested power, digital, and software systems for F-18 radar jammers and decoy systems.

Capstone Design Project: Good Morning Alarm

Boston, MA

Project Lead for Electrical and Mechanical Solutions

05/2008 – 04/2009

- Designed Bluetooth/accelerometer interface for intelligent alarm clock.

Protonex

Southborough, MA

Coop Electrical Engineer

07/2008 – 12/2008

- Designed PCB for hand-held LCD control unit.

Raytheon IDS EDD

Sudbury, MA

Tech Student Junior

07/2007 – 01/2008

- Tested FPGA Verilog code using Cypress' Warp Simulator.

Bose

Firmware Verification Engineer

Framingham, MA

07/2006 – 12/2006

- Performed regression tests on car audio systems to verify new firmware builds.

Education

Northeastern University

Bachelor of Science in Electrical Engineering with minor in Mathematics

Boston, MA

May 2009

- Summa Cum Laude, Honors, GPA: 3.9

Publications

Microsoft SharePoint 2010: “No Code” Web Design for Collaboration and Information Management, 1st

Author

- Presented at ISaC Symposium 2011, El Segundo, CA

06/2011